

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: April 21, 2006

Signature: 

(Marco Jimenez)

Docket No.: 333772000420

Client reference: 10958-USA-1-(2)

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re CIP Patent Application of:

Rochit RAJSUMAN et al.

Confirmation No.: 7594

Application No.: 10/759,462

Examiner: Phung M. Chung

Filed: January 16, 2004

Art Unit: 2138

For: SEMICONDUCTOR TEST SYSTEM
STORING PIN CALIBRATION DATA,
COMMANDS AND OTHER DATA IN NON-
VOLATILE MEMORY

**TRANSMITTAL OF SUPPLEMENTAL DECLARATION AND
REQUEST FOR CORRECTION OF THIRD INVENTOR'S FIRST NAME**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

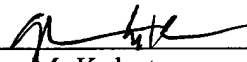
Enclosed are a Supplemental Declaration duly executed by the Applicants, and a Supplemental Application Data Sheet, for filing with the above-identified patent application. These documents are being filed in order to correct the spelling of the third co-inventor's first name from Hiroki Yamoto to Hiroaki Yamoto.

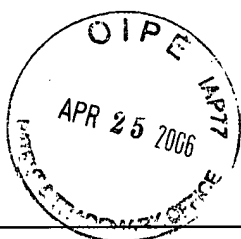
Applicants respectfully request the issuance of a Corrected Filing Receipt. Enclosed for your convenience is a marked up copy of the updated filing receipt dated June 4, 2004.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, Applicants petition for any required relief including extensions of time and authorizes the Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing Docket No. 333772000420.

Dated: April 21, 2006

Respectfully submitted,

By 
Glenn M. Kubota
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**SUPPLEMENTAL DECLARATION FOR
UTILITY OR DESIGN
PATENT APPLICATION
(37 CFR 1.63)**

☐ Declaration
Submitted
with Initial
Filing

OR

☒ Declaration
Submitted after Initial
Filing (surcharge (37 CFR 1.16 (e))
required)

Attorney Docket No.	333772000420
First Named Inventor	Rochit RAJSUMAN et al.
COMPLETE IF KNOWN	
Application Number	10/759,462
Filing Date	January 16, 2004
Art Unit	2133
Examiner Name	Unknown

AS A BELOW-NAMED INVENTOR, I HEREBY DECLARE THAT:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: **SEMICONDUCTOR TEST SYSTEM STORING PIN CALIBRATION DATA, COMMANDS AND OTHER DATA IN NON-VOLATILE MEMORY**, the specification of which is attached hereto unless the following box is checked:

☒ was filed on January 16, 2004 as United States Application Serial No. 10/759,462. or PCT International Application No. _____ and was amended on * (if applicable).

I HEREBY STATE THAT I HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE.

I acknowledge the duty to disclose information which is material to the patentability as defined in 37 C.F.R. § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Application No.	Country	Date of Filing (day/month/year)	Priority Claimed?
*			<input type="checkbox"/> Yes <input type="checkbox"/> No
			<input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby claim benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

Application Serial No.	Filing Date

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

Application Serial No.	Filing Date	Status
10/340,349	January 10, 2003	<input type="checkbox"/> Patented <input checked="" type="checkbox"/> Pending <input type="checkbox"/> Abandoned
09/547,752	April 12, 2000	<input checked="" type="checkbox"/> Patented <input type="checkbox"/> Pending <input type="checkbox"/> Abandoned

I hereby appoint:

all Practitioners associated with **Customer No. 25224**,

all of Morrison & Foerster LLP, 555 West Fifth Street, Los Angeles, CA 90013, USA, jointly, and each of them severally, as my attorneys at law/patent agent(s), with full power of substitution, delegation and revocation, to prosecute this application, to make alterations and amendments therein, to receive the patent, and to transact all business in the U.S. Patent and Trademark Office connected therewith.

Please direct all communications to: **Glenn M. Kubota**, at Customer No. 25224, whose address is:

Morrison & Foerster LLP
555 West Fifth Street
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Please direct all telephone calls to: Glenn M. Kubota at (213) 892-5752.

Please direct all facsimiles to: Glenn M. Kubota at (213) 892-5454.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

3/6/06
Date

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Supplemental Application Data Sheet

Application Information

Application No.:	<u>10/759,462</u>
Application Type:	Regular
Subject Matter:	Utility
Suggested Group Art Unit:	<u>2138</u>
CD-ROM or CD-R?:	None
Sequence submission?:	None
Computer Readable Form (CRF)?:	No
Title:	SEMICONDUCTOR TEST SYSTEM STORING PIN CALIBRATION DATA, COMMANDS AND OTHER DATA IN NON- VOLATILE MEMORY
Attorney Docket Number:	333772000420
Request for Early Publication?:	No
Request for Non-Publication?:	No
Total Drawing Sheets:	13
Small Entity?:	No
Petition included?:	No
Secrecy Order in Parent Appl.?:	No

Applicant Information

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 Country of mailing address:: US
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Correspondence Information

Correspondence Customer Number:: 25224

Representative Information

Representative Customer Number:: 25224

Domestic Priority Information

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	continuation-in-part	10/340,349	01/10/2003
10/340,349	continuation-in-part	09/547,752	04/12/2000

Assignee Information

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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
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 Alexandria, Virginia 22313-1450
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/759,462	01/16/2004	2133	986	333772000420	13	8	4

CONFIRMATION NO. 7594

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 LOS ANGELES, CA 90013-1024



UPDATED FILING RECEIPT



OC000000012867051

Date Mailed: 06/04/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Rochit Rajsuman, Santa Clara, CA;
 Robert Sauer, Santa Clara, CA;
~~Hiroki Yamoto~~, Santa Clara, CA;

HIROAKI

DOCKETED 

Assignment For Published Patent Application

ADVANTEST CORPORATION, Ora-gun, JAPAN;

Domestic Priority data as claimed by applicant

This application is a CIP of 10/340,349 01/10/2003
 which is a CIP of 09/547,752 04/12/2000 PAT 6,567,941

Foreign Applications

If Required, Foreign Filing License Granted: 04/23/2004

Projected Publication Date: 09/09/2004

Non-Publication Request: No

Early Publication Request: No

RECEIVED
 JUN - 9 2004
 MORRISON & FOERSTER LLP
 LOS ANGELES

Title

Semiconductor test system storing pin calibration data, commands and other data in non-volatile memory

Preliminary Class

714

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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